

## **JEFFREY H. DROBMAN, PHD**

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### **Professional Summary**

Dr. Jeff Drobman has over 35 years of industry and academic experience covering a broad swath of hardware and software engineering, having worked in the computer chip industry for 16 years (at AMD, Integrated Device Technology, ComCore/National Semi and Semtech), along with a 6 year stint in aerospace (Hughes Aircraft Doctoral Fellow).

Dr. Drobman is a long-time expert on microprocessor and microcontroller architecture, interfaces and programming. He has also been a software developer (currently), with attendant experience doing product planning, marketing, design and support. He enjoyed a lengthy career as an applications engineer (FAE)/manager at 3 IC/chip companies, and was a product marketing manager at a fourth.

Dr. Drobman has a unique perspective on the computer chip industry, having been involved since the early days of Fairchild Semi in the mid 1960s (while still in high school), then observing the founding of Advanced Micro Devices in May 1969, for whom he later was employed as a field applications engineer for software and systems. (Dr. Drobman has chronicled the "genesis" of Silicon Valley in an article plus org chart.)

Dr. Drobman has been engaged as an Expert Witness for several technology related cases over the past several years, including producing written opinions (declarations), with experience being deposed.

Dr. Drobman is a UCLA triple alumnus (BS, MS, PhD), former UCLA Lecturer, and founding board member of the UCLA Engineering Alumni Assn.

### **Expertise**

For 12 years as an FAE providing technical support for all IC products for customers all across the western US, Dr. Drobman's chief focus was microprocessors. He supported the i8080/85, Z80, i8048 and Z8000 MPU/MCUs while an FAE at AMD, and the MIPS R2000/3000/4000/5000 embedded RISC CPU/microcontrollers at IDT.

Dr. Drobman supported customers to develop, integrate and test/debug their C/C++ and assembly code on various Unix workstations, including using: simulators, multi-level debuggers, disassemblers, ICE and logic analyzers; and RTOS such as VxWorks and WindRiver. Dr. Drobman has programming experience handling interrupts and system events, including recent years developing object-oriented code with .NET (Visual Basic).

The primary market segments for the MIPS embedded RISC CPU/microcontrollers were: laser printers, network routers, set-top boxes. Dr. Drobman gave seminars on the MIPS RISC architecture to all IDT customers in these segments. He also gave MIPS seminars at these universities: UCLA, Arizona State, Cal State SLO.

As an Applications Engineering Manager, Dr. Drobman was responsible for Ethernet IC products (his FAQ on "Baseline Wander" is referenced in a Lucent patent application). As a Telecom Marketing Manager, Dr. Drobman was responsible for all aspects of a SONET/SDH optical networking product line, specializing in network synchronization.

## Education

<u>Year</u>	<u>College or University</u>	<u>Degree</u>
1980	University of California, Los Angeles	Ph.D., Computer Science
1973	University of California, Los Angeles	M.S., Computer Science
1970	University of California, Los Angeles	B.S., Engineering (cum laude)